

Claims

- [c1] A multistaged amplification circuit comprising:
 - a differential amplification circuit, equipped with n differential amplifiers that are connected in a multistaged manner and that amplify and output an input signal from a previous stage to a following stage;
 - a plurality of transistors connected to said n differential amplifiers that are connected in a multistaged manner, and connected to one constant current source by a current mirror; and
 - wherein said plurality of transistors are arranged collectively on the side of said constant current source.
- [c2] The multistaged amplification circuit according to claim 1, wherein said constant current source is arranged on either an input side or an output side of said differential amplification circuit.
- [c3] The multistaged amplification circuit according to claim 1, wherein said constant current source is arranged almost in the center of said n differential amplifiers connected in a multistaged manner.
- [c4] The multistaged amplification circuit according to claim

1, wherein said plurality of transistors are grounded to the same place via separate ground lines.

- [c5] A multistaged amplification circuit, comprising:
 - a differential amplification circuit, equipped with n differential amplifiers that amplify and output an input signal from a previous stage to a following stage, and that are connected in a multistaged manner;
 - a plurality of transistors connected to a plurality of differential amplifiers and one constant current by a current mirror for every group into which said n plurality of differential amplifiers are divided; and
 - wherein said plurality of transistors are arranged collectively on the side of said constant current source for every said group.
- [c6] A multistaged amplification circuit comprising:
 - a differential amplification circuit, equipped with n differential amplifiers that amplify and output an input signal from a previous stage to a following stage, and that are connected in a multistaged manner;
 - a plurality of transistors connected to said n differential amplifiers that are connected in a multistaged manner, and that are connected to one constant current source by a current mirror; and
 - wherein said plurality of transistors are grounded to the same place via separate ground lines.

